



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : 2183
Assignee : Intel Corporation Examiner : Daniel H. Pan
Serial No. : 10/070,091
Filed : February 27, 2002
Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF AUGUST 29, 2005

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

11/23/2005
Date of Deposit

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Deborah R. Nast

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